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## APPLICANTS

Hiraku Chakihara, Akishima, JAPAN;  
Kousuke Okuyama, Kawagoe, JAPAN;  
Masahiro Moniwa, Sayama, JAPAN;  
Makoto Mizuno, Kokubunji, JAPAN;  
Keiji Okamoto, Higashiyamato, JAPAN;  
Mitsuhiko Noguchi, Sagamihara, JAPAN;  
Tadanori Yoshida, Sayama, JAPAN;  
Yasuhiko Takahshi, Higashiyamato, JAPAN;  
Akio Nishida, Tachikawa, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

None. *EP*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-224254 07/31/2002 verified. *EP*  
JAPAN 2003-97210 03/31/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/23/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	104	53	19
Verified and Acknowledged	<i>EP</i>	Examiner's Signature	Initials		

## ADDRESS

020457

## TITLE

Semiconductor memory device and a method of manufacturing the same, a method of manufacturing a vertical MISFET and a vertical MISFET, and a method of manufacturing a semiconductor device and a semiconductor device

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